

FIG. 1

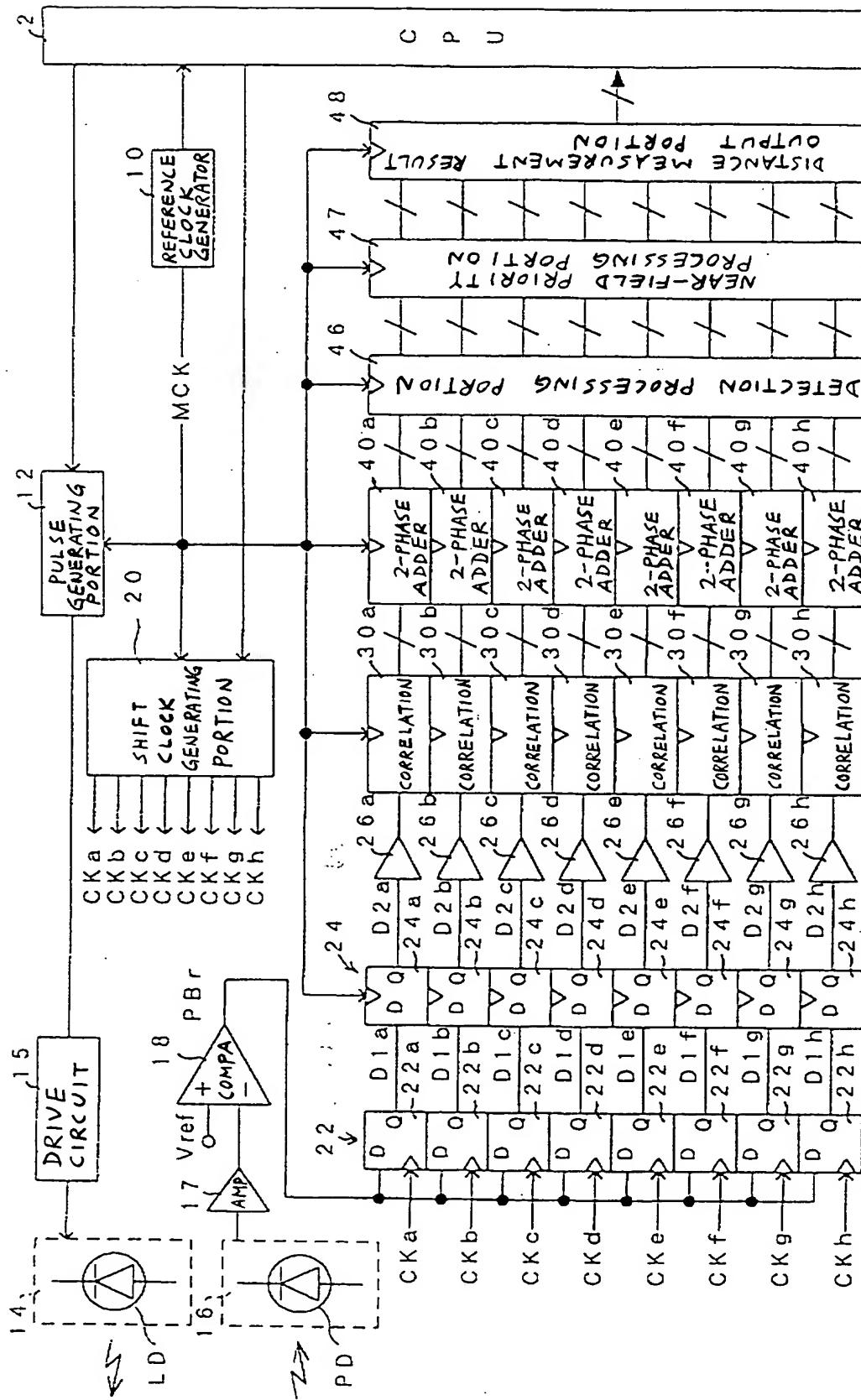


FIG. 2

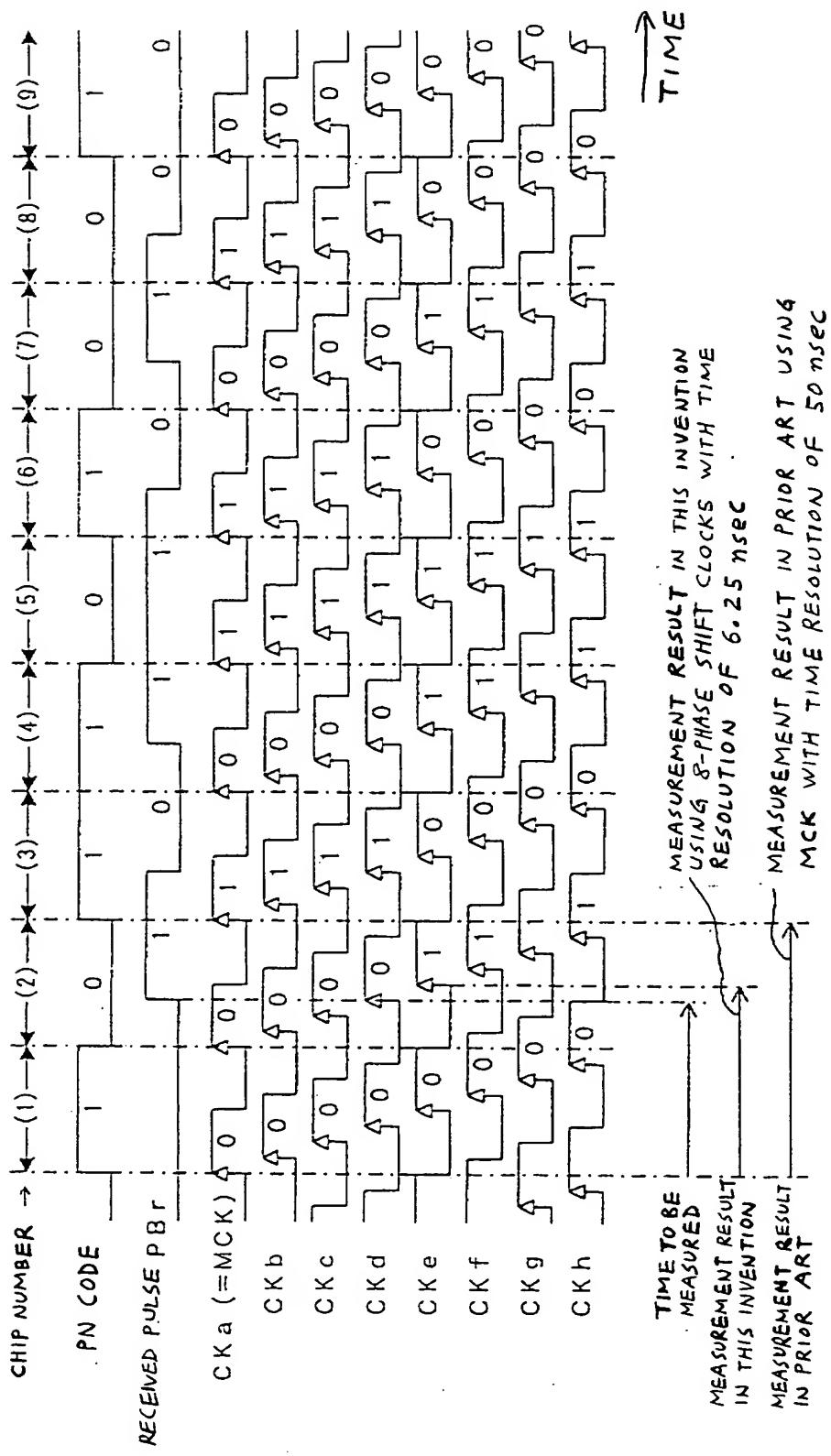


FIG. 3

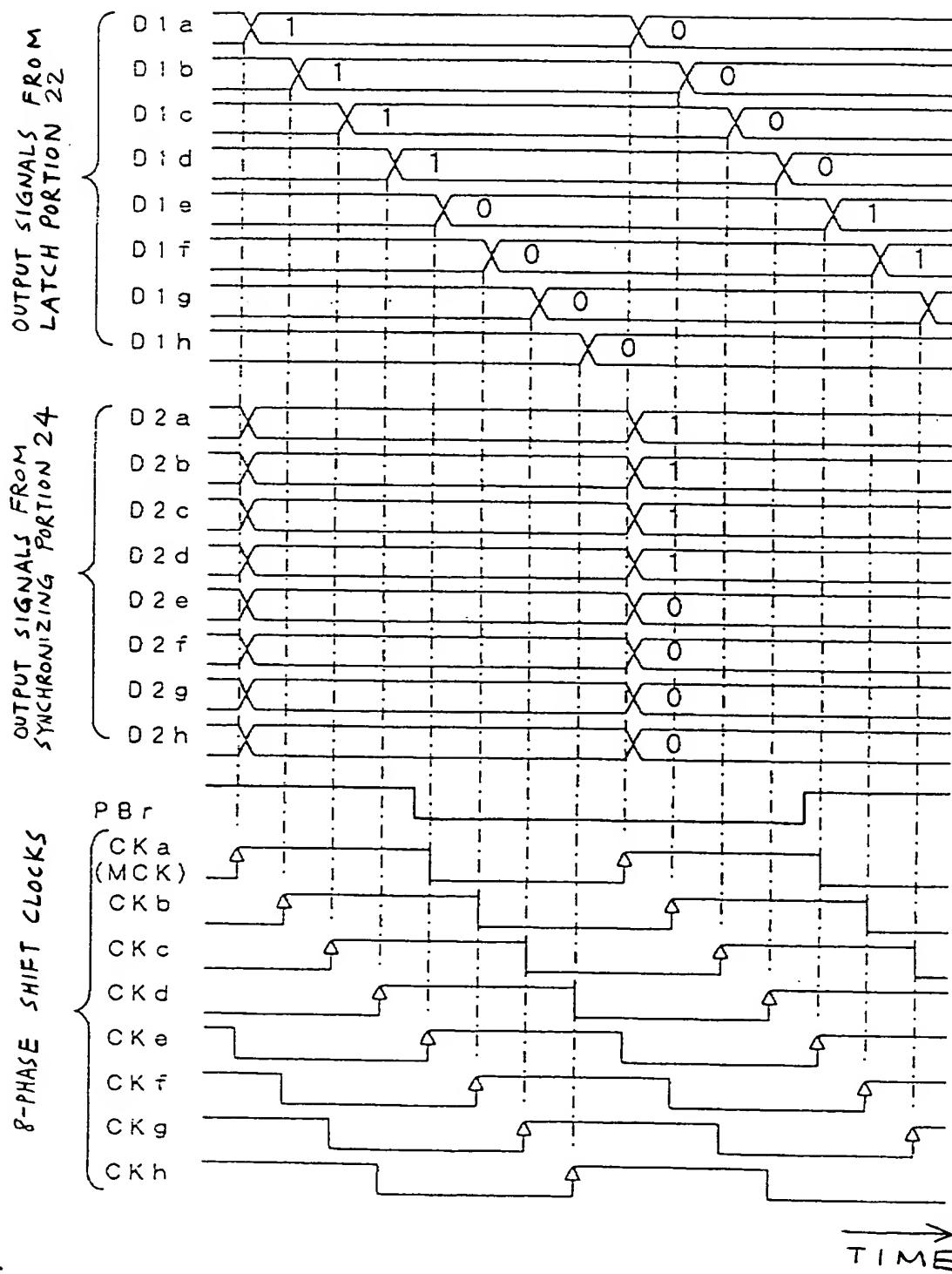


FIG. 4

INTEGRATED CIRCUIT DESIGN

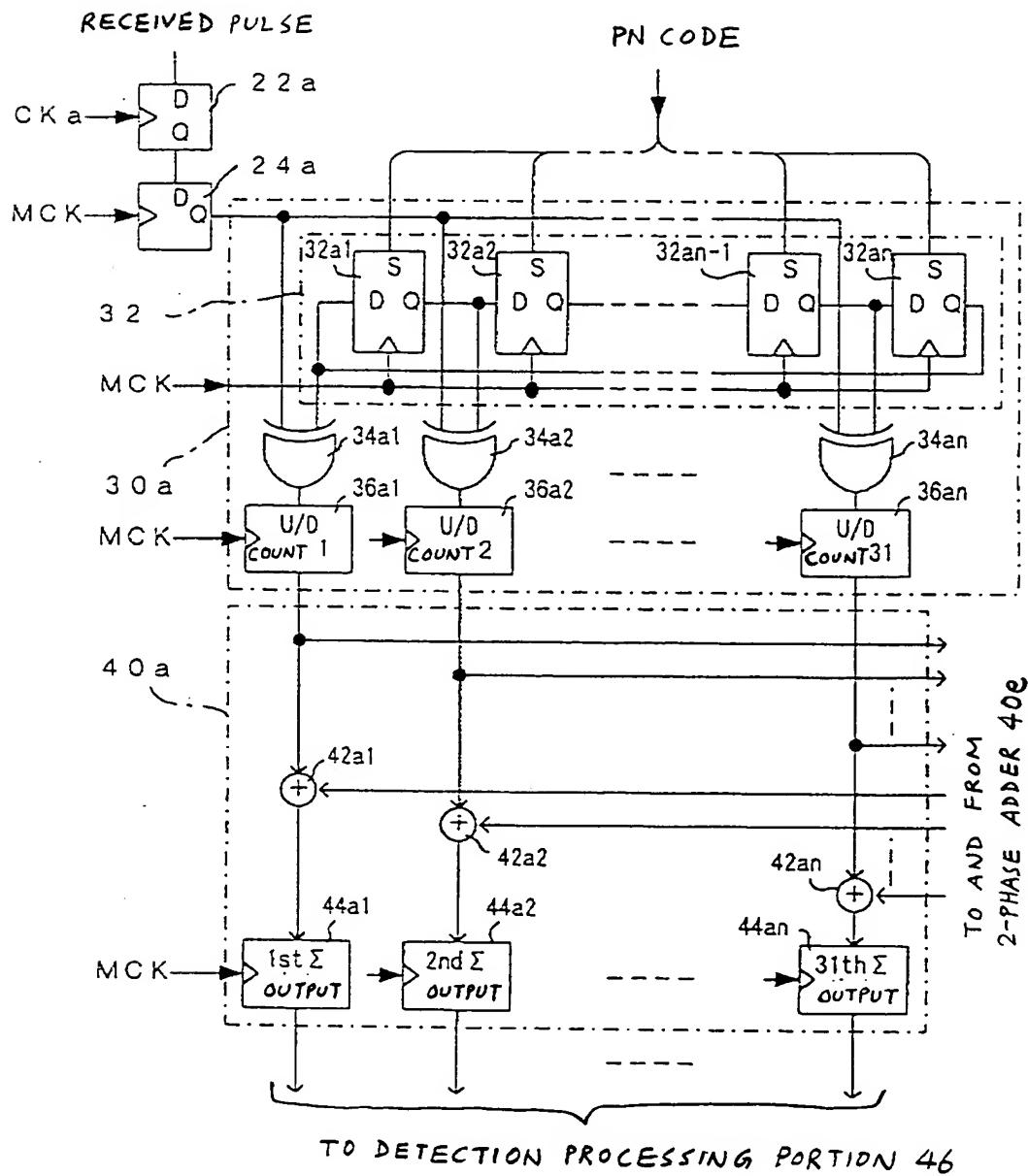


FIG. 5

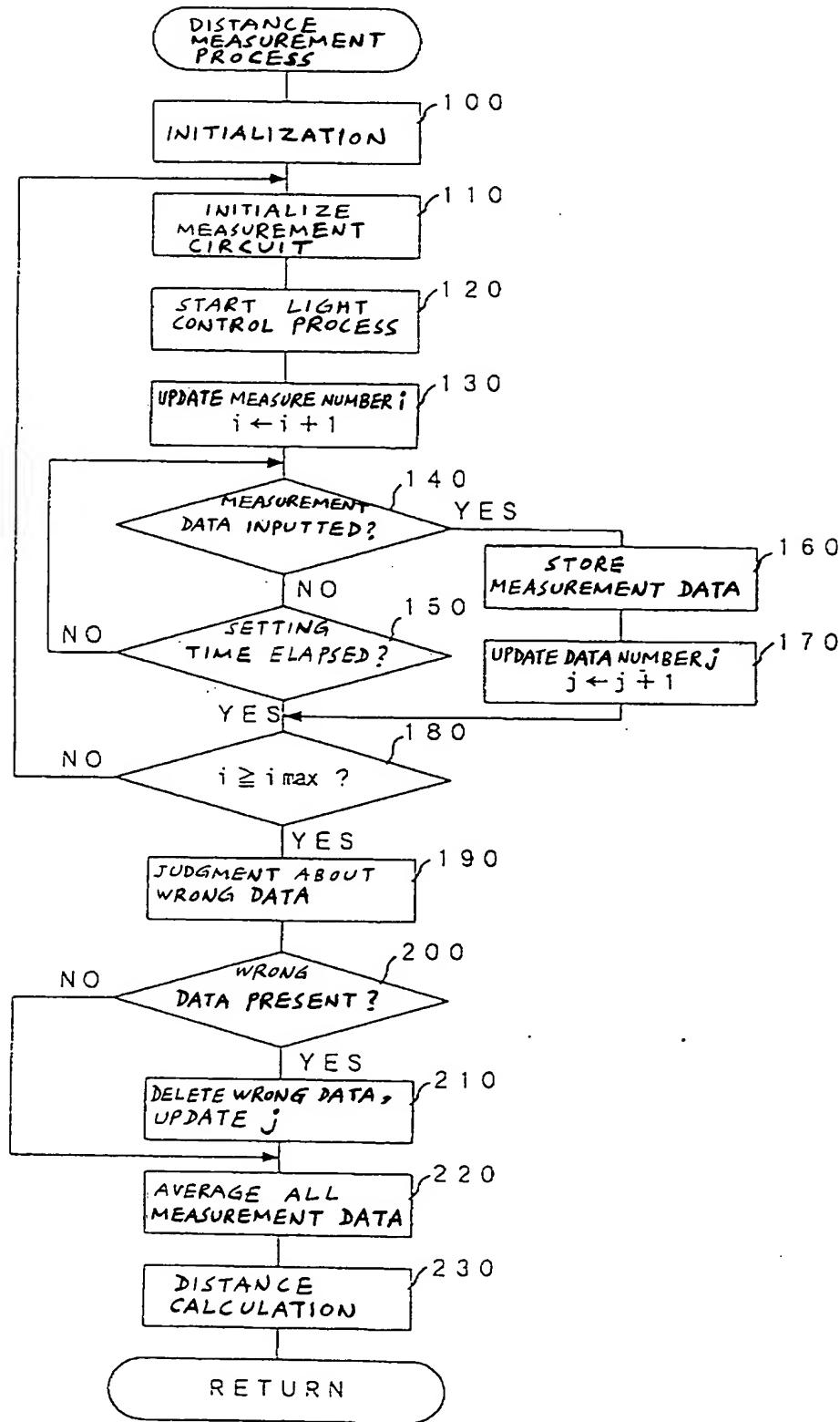


FIG. 6

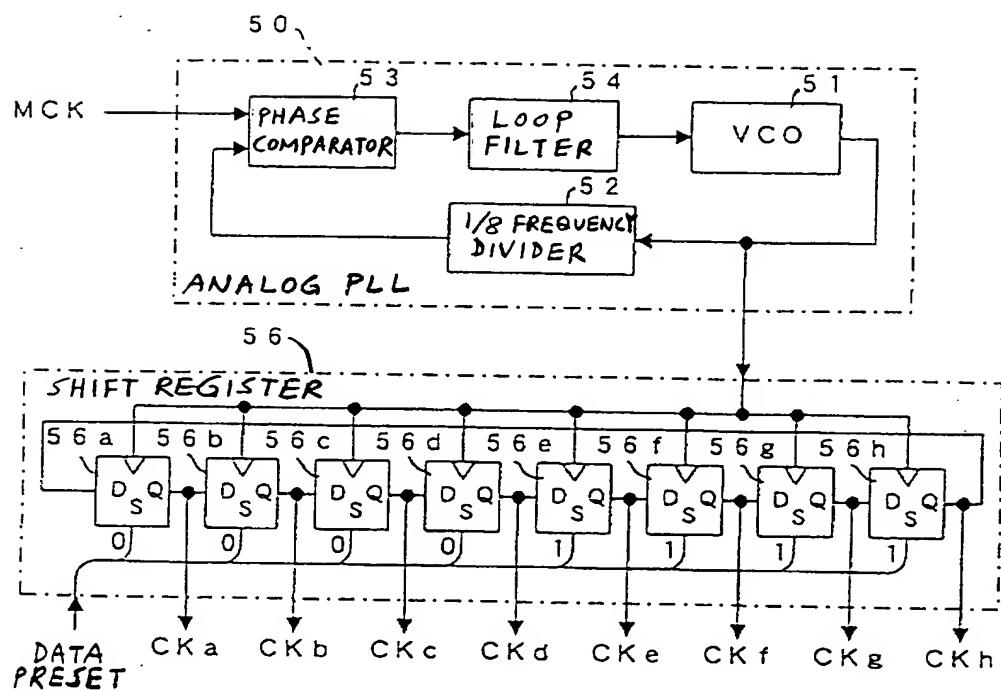
20

FIG. 7

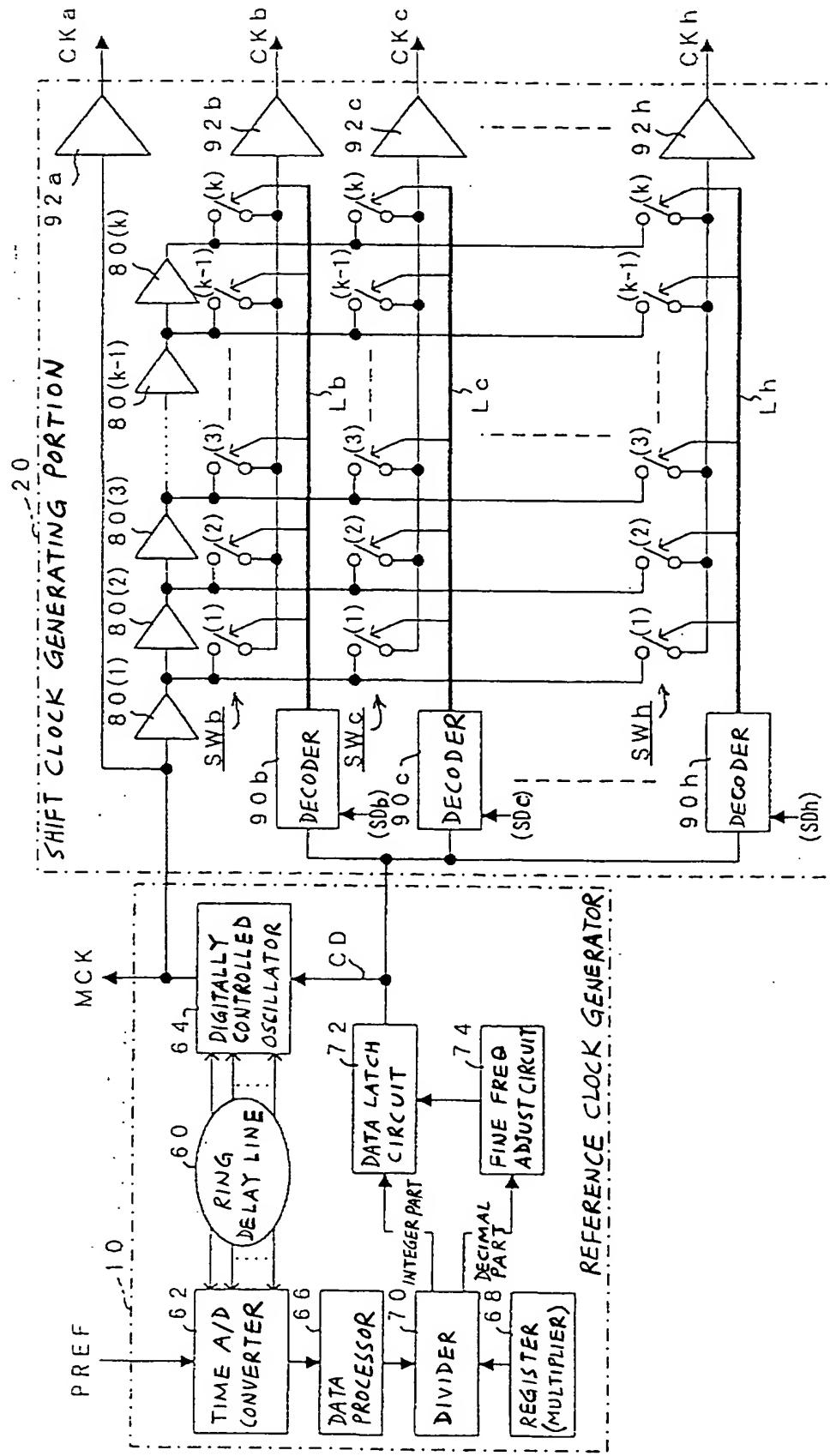


FIG. 8

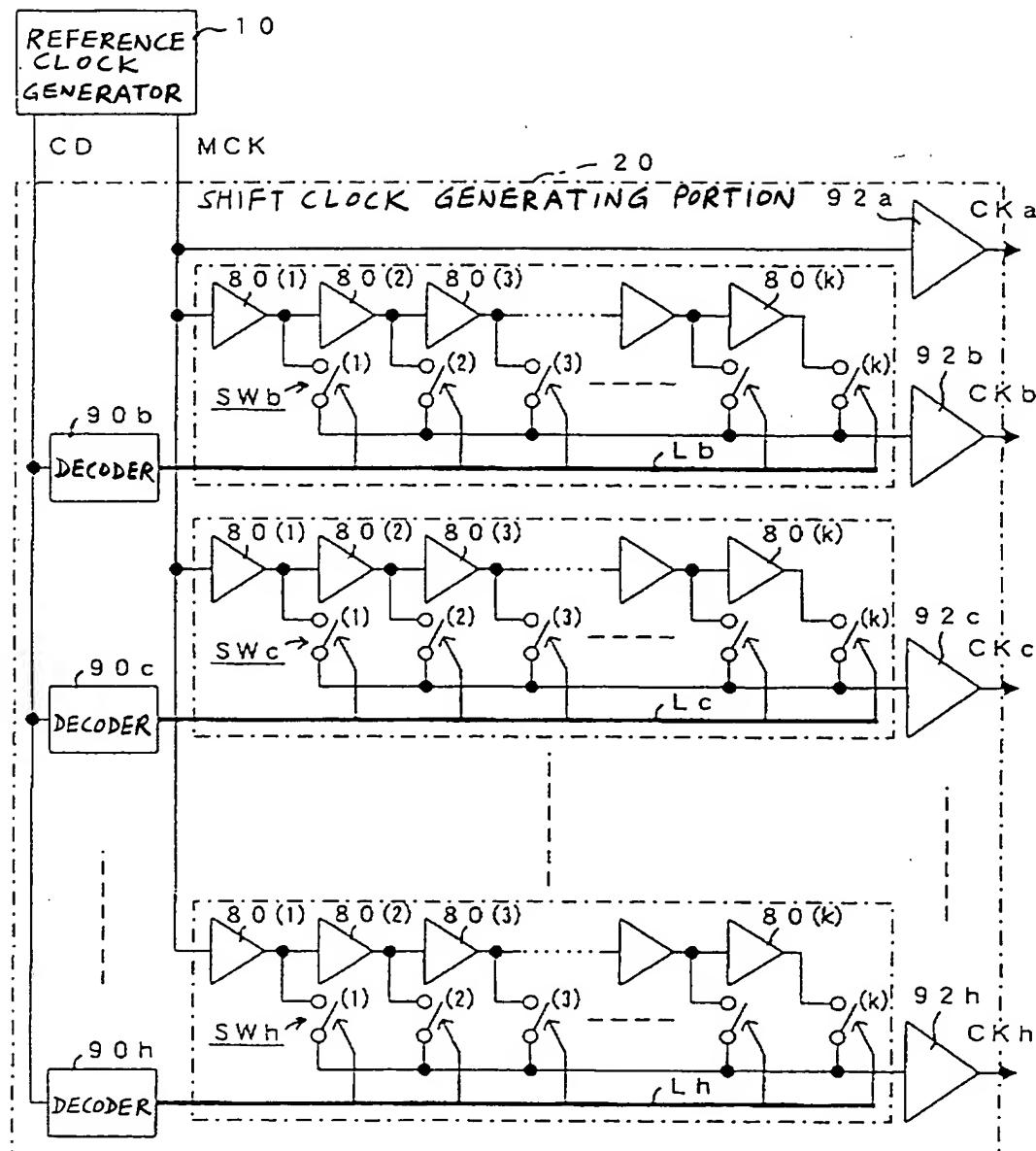


FIG. 9

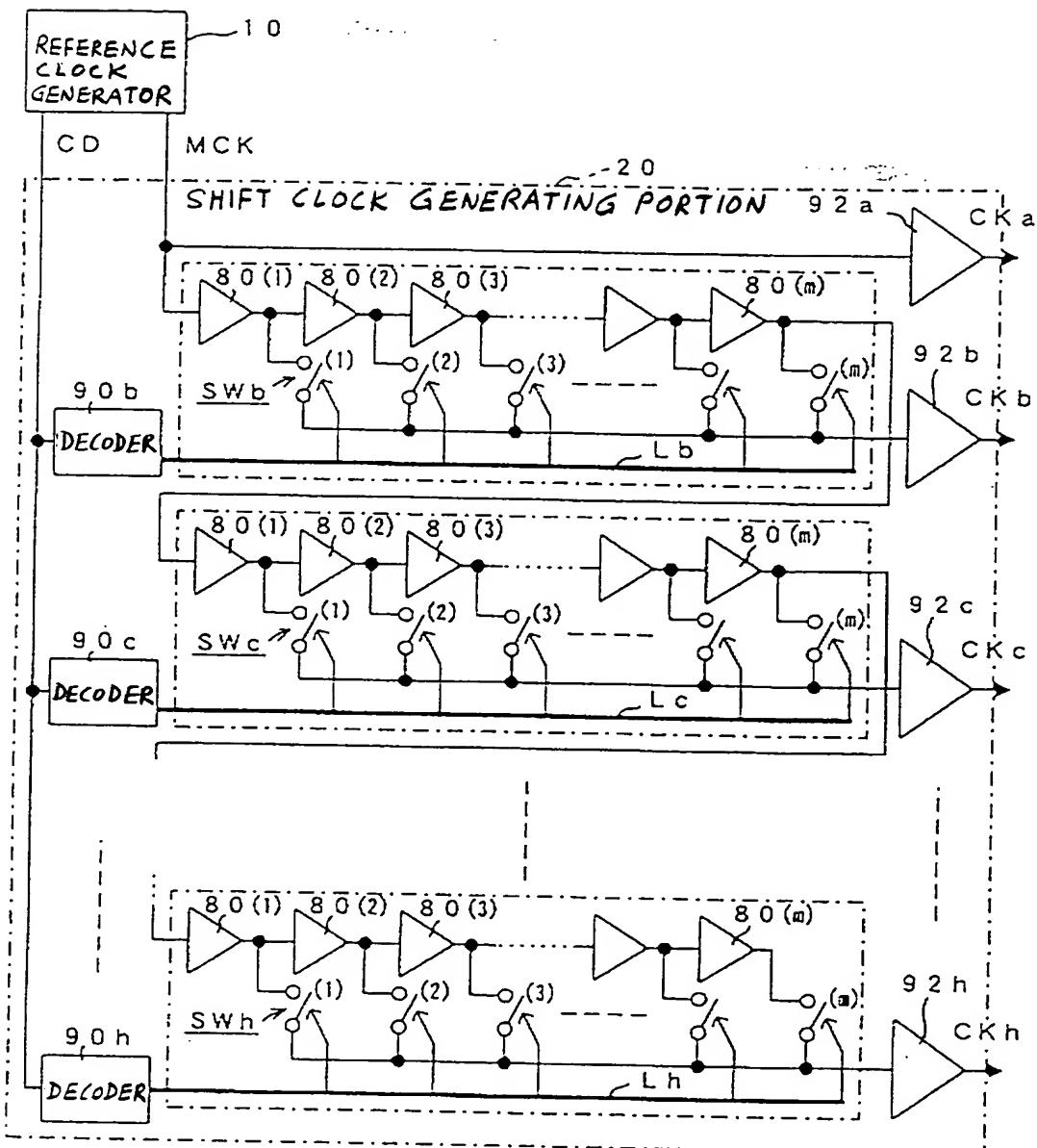


FIG. 10

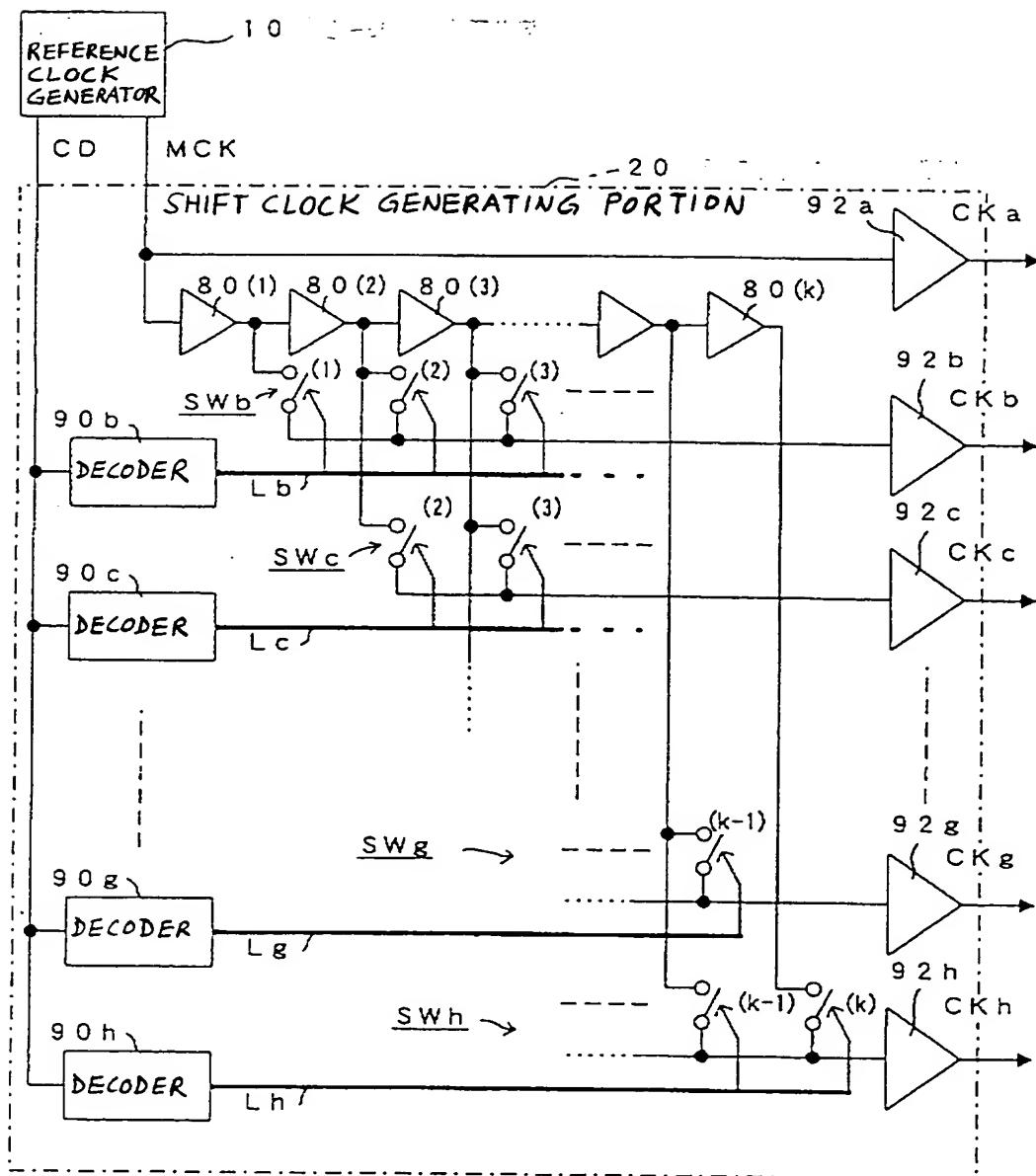


FIG. 11

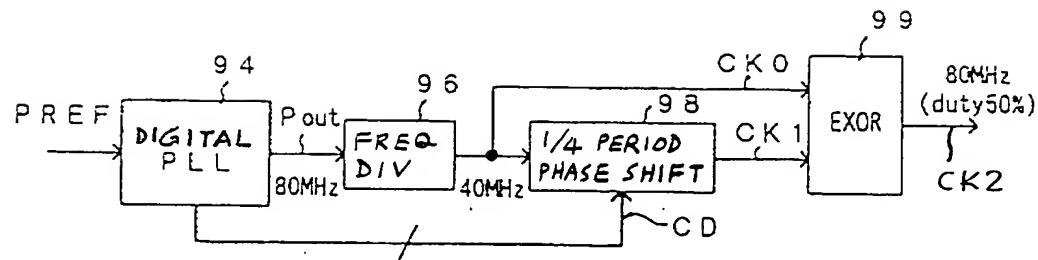
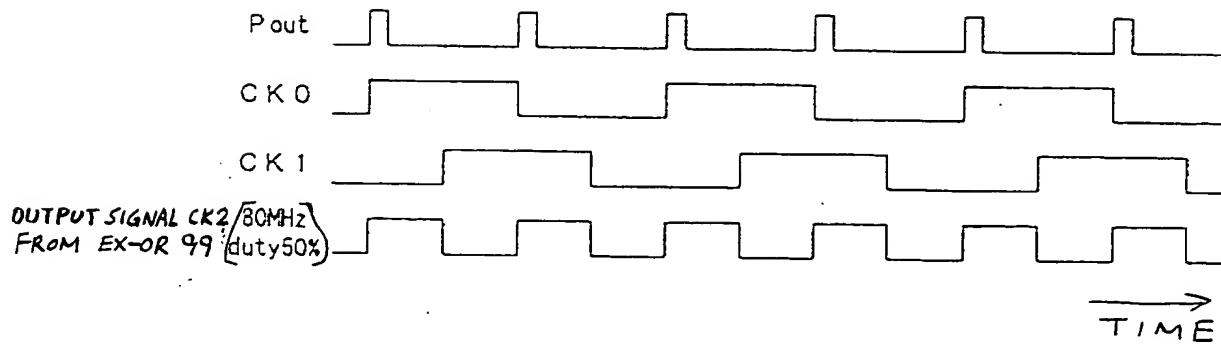


FIG. 12



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